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OLIFF & BERRIDGE, PLC

ATTORNEYS AT LAW

Supplemental Application Data Sheet

Application Information

Application Type::

Regular

Subject Matter::

Utility

CD-ROM or CD-R:

None

Title::

TRANSISTOR, INTEGRATED CIRCUIT,

ELECTRO-OPTIC DEVICE, ELECTRONIC

INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR

Attorney Docket Number::

117849

Total Drawing Sheets::

10

Small Entity::

No

Applicant Information

Applicant Authority type::

Inventor

Primary Citizenship Country::

Japan

Status::

Full Capacity

Given Name::

Ichio

Family Name::

YUDASAKA

City of Residence::

Suwa-shi Chino-shi

State or Province of Residence::

Nagano-ken

Country of Residence::

Japan

Applicant Authority type::

Inventor

Primary Citizenship Country::

Japan

Status::

Full Capacity

Given Name::

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Family Name::

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City of Residence::

Suwa-shi Chino-shi

State or Province of Residence::

Nagano-ken

Country of Residence::

Japan

Applicant Authority type::

Inventor

Primary Citizenship Country::

Japan

Status::

Full Capacity

Given Name::

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Family Name::

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City of Residence::

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State or Province of Residence::

Nagano-ken

Country of Residence::

Japan

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Foreign Priority	/ Information		
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-351112	12/03/2002	Yes
Assignee Inform	mation		
Assignee Name::		Seiko Epson Corporation	
Street of mailing address::		4-1, Nishishinjuku 2-chome, Shinjuku-ku	
City of mailing address::		Tokyo	
Country of mailing address::		Japan	